				Complete if Known			
				Application Number	10	1730,387	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)			Filing Date	/			
			First Named Inventor	Mahibur Rah	man et al.		
			Group Art Unit	26	11		
			Examiner Name	LF			
Sheet	1	of	1	Attorney Docket Number	SC12882ZC		

			J. S. PATENT DOCUM	MENTS	•		
Examiner	Cite No. 1	Document Number	Publication Date		of Patentee or Applicant	Pages, Columns, Lines, Where Re	
Initials*		Number -Kind Code <sup>2</sup> (il known)	MM-DD-YYYY		of Cited Document	Passages or Relevant Figures Ap	pear
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	FOREIGN PATENT DOCUMENTS							
Examine r Initials*	Cite No. 1	Foreign Patent Document Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T 6		
		NON PATENT L	ITERATURE DOCUM	ENTS				

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Examiner Initials	Cite No. 1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.				
LF	AA	LINDFORS et al., "A 3-V Continuous-Time Filter with On-Chip Tuning for IS-95," IEEE JOURNAL OF SOLID-STATE CIRCUITS, VOL. 34, NO. 8, AUGUST 1999, pp. 1150-1154.				
LF	AB	YAMAZAKI et al., "WP23.1 A 450kHz CMOS Gm-C Bandpass Filter with +/-0.5% Center Frequency Accuracy for On-Chip PDC IF Receivers," 1999 IEEE INTERNATIONAL SOLID-STATE CIRCUITS CONFERENCE, ISSCC99/SESSION 23/PAPER WP 23.1, pp. 392-393, p. 482.				
LF	AC	DURHAM et al., "Circuit Architectures for High Linearity Monolithic Continuous-Time Filtering," IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS—II: ANALOG AND DIGITAL SIGNAL PROCESSING, VOL. 39, NO. 9, SEPTEMBER 1992, pp. 651-657.				

Examiner	/Leon Flores/	Date	01/25/2007
Signature	, 20011 5 25 25 3,	Considered	01/23/200.

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